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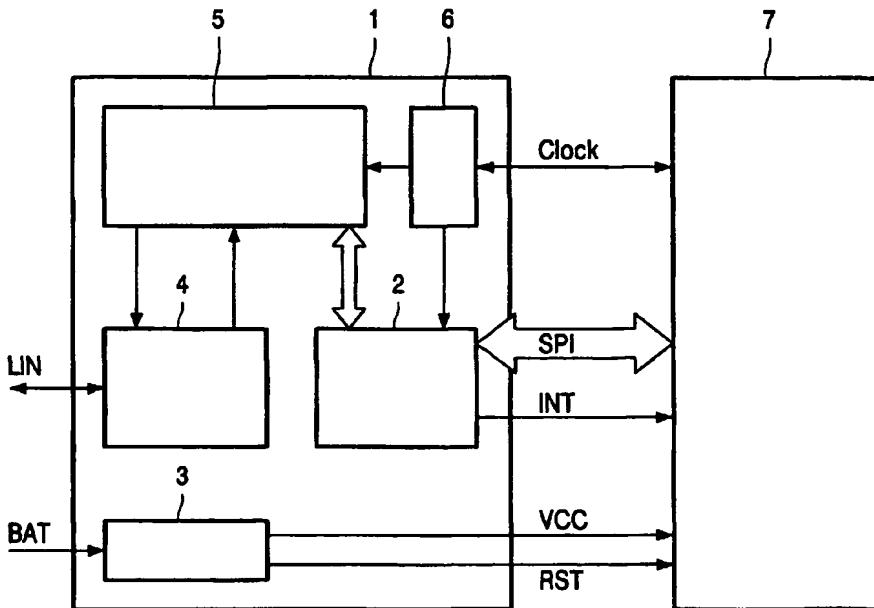
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(54) Title: INTEGRATED CIRCUIT



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(57) Abstract: An integrated circuit having a system base chip (1) that has basic functions for a transmitting and/or receiving system for a vehicle data bus, namely at least a system voltage supply (3), a system reset (3) and a monitoring function (2),—an interface circuit (4, 5) that, in a self-contained fashion, runs at least parts of a data bus protocol, and in particular the LIN (Local Interconnect Network) protocol, that performs detection of the bit-rate of received data, and that is capable of passing on at least one received or transmitted byte.—a serial/parallel converter (5) that makes use in its conversion of the bit-rate detected by the interface circuit (4, 5).



For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

IB 03/04838

A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 G06F15/78 H04L25/02 H04J3/06

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6 049 888 A (CHANOUX ET AL) 11 April 2000 (2000-04-11) column 2, line 62 - column 4, line 9; figure 1	1-6
A	GRILL W: "SYNCHRONIZING CONTROLLER DETECTS BAUD RATE" EDN ELECTRICAL DESIGN NEWS, CAHNERS PUBLISHING CO. NEWTON, MASSACHUSETTS, US, no. EUROPE, 2 March 1998 (1998-03-02), page 99, XP000779115 ISSN: 0012-7515 the whole document	1-6
A	EP 1 206 092 A (TEXAS INSTRUMENTS INC) 15 May 2002 (2002-05-15) page 3, line 6 - line 16 page 5, line 1 - line 7	1
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Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	"SERIELLE SCHNITTSTELLEN" CHIP ZEITSCHRIFT FUER MIKROCOMPUTER-TECHNIK, VOGEL VERLAG. WURZBURG, DE, no. 2, February 1983 (1983-02), pages 142-145, XP000761493 ISSN: 0170-6632 the whole document -----	1

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EP 1206092	A 15-05-2002	EP 1206092 A2 JP 2002204226 A	15-05-2002 19-07-2002